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Class	Subclass
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ISSUE CLASSIFICATION

U.S. **UTILITY** Patent Application

<p>45 O.I.P.E.</p> <p>SCANNED 15-4 Q.A. Am</p>	PATENT DATE
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APPLICATION NO. 09/853989	CONT/PRIOR D	CLASS 712	SUBCLASS	ART UNIT 2183	EXAMINER
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APPLICANTS

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Gerald Pechanek
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TITLE

Methods and apparatus for power control in a scalable array of processor elements

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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